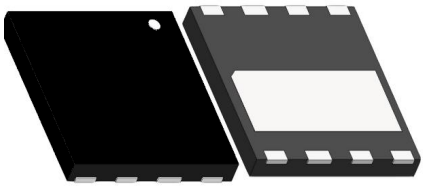
E-mode 650V,30A,80mΩ GaN HEMT

Description

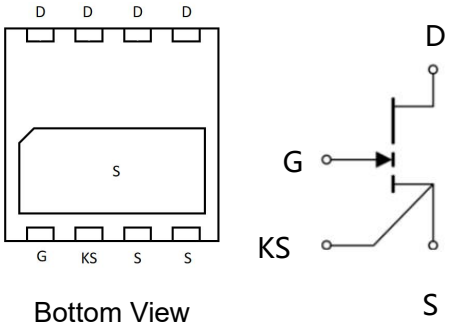
TK65F080 is an enhancement mode GaN-on-silicon transistor. GaN is a wide band gap semiconductor with high power density. The gallium nitride transistor is characterized by no body diode, so the reverse recovery charge is zero.

Features

- 650 V enhancement mode power switch
- $R_{DS(on)}$ =80mΩ
- $I_{DS(max)}$ = 30A
- Easy gate drive requirements (0 V to 6 V)
- Very high switching frequency (> 10 MHz)
- Fast and controllable fall and rise times
- Zero reverse recovery loss



DFN8x8



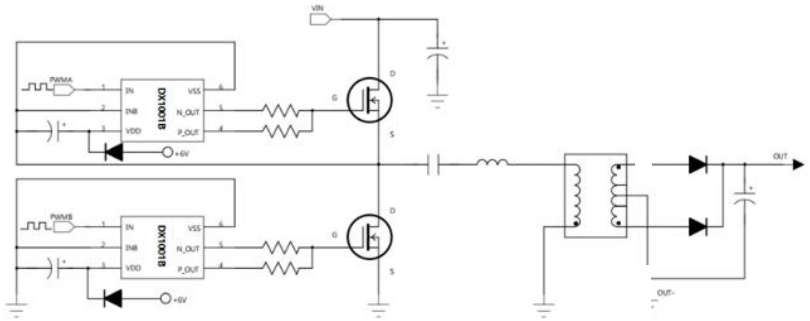
Bottom View

Applications

- Fast Battery Charging
- LED lighting drivers
- Power Factor Correction
- LLC Converters
- Wireless Power Transfer

Device Information

Part Number	Marking Code	Package	Packing
TK65F080	65F080	DFN8x8	260pcs/tray



Typical application circuit for LLC

Absolute Maximum Ratings (Tc=25℃ unless otherwise specified)

Parameter	Symbol	Value	Unit	Condition
Drain-Source voltage	V_{DS}	650	V	
Gate-source voltage	V_{GS}	-7 to 6	V	
Continuous drain current*	I_D	30	A	Tc=25℃
		29	A	Tc=125℃
Operation and storage temperature	T_j	-55 to 150	℃	
	T_{stg}	-55 to 150	℃	

*** An Estimated Value**

Electrical Characteristics (Tc=25℃ unless otherwise specified)

Typical Performance – Static

Parameter	Symbol	Values			Unit	Test condition
		Min.	Type.	Max.		
Drain source breakdown voltage	BV_{DS}	650	/	/	V	VGS=0V, ID=20μA
Total drain leakage current	I_{DSS}	/	/	65	μA	VDS=650V, VGS=0V, Tj=25℃
		/	/	390	μA	VDS=650V, VGS=0V, Tj=150℃
Gate-to-source current	I_{GSS}	/	163	/	μA	VDS=0V, VGS=6V, Tj=25℃
Static drain-source on-resistance	$R_{DS(ON)}$	/	60	80	mΩ	VGS=6V, ID=8A, Tj=25℃
		/	120	/	mΩ	VGS=6V, ID=8A, Tj=150℃
Gate threshold voltage	$V_{GS(th)}$	1.2	1.7	2.5	V	VDS=VGS, ID=30.7mA,

Typical Performance – Dynamic

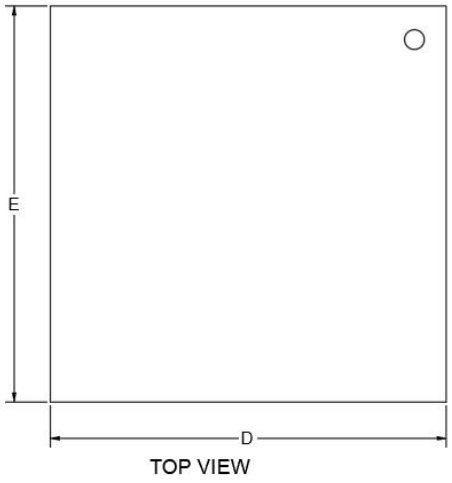
Parameter	Symbol	Values			Unit	Test condition
		Min	Type	Max		
Input capacitance	C_{ISS}	/	240	/	pF	V_{DS}=400V, V_{GS}=0V, f=100kHz
Output capacitance	C_{OSS}	/	103	/	pF	
Reverse transfer capacitance	C_{RSS}	/	1	/	pF	
Output capacitance, energy related	$C_{OSS(er)}$	/	129	/	pF	V_{DS}=0V to 400V,V_{GS}=0V
Output capacitance time related	$C_{OSS(tr)}$	/	179	/	pF	
Total gate charge	Q_G	/	6	/	nC	V_{DS}=400V, V_{GS}=0V to 6V
Gate-drain charge	Q_{GD}	/	2.7	/	nC	
Gate-source charge	Q_{GS}	/	1.2	/	nC	
Gate Resistance	R_G	/	3.5	/	Ω	f =5MHZ, Open drain

Thermal characteristics

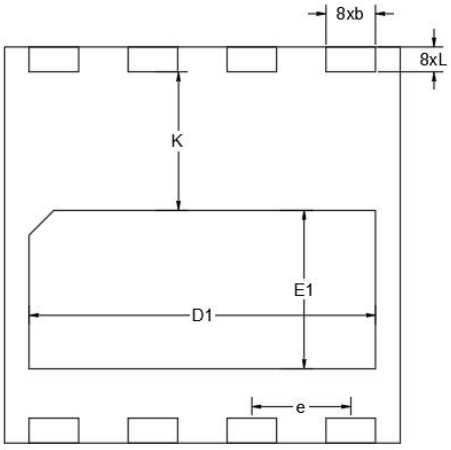
Parameter	Symbol	Values	Unit	Note/Test Condition
Thermal resistance, junction-ambient	R_{thJA}	57.6	°C/W	
Thermal resistance, junction-case	R_{thJC}	1.1	°C/W	
Maximum reflow soldering temperature	T_{sold}	260	°C	MSL3

Package

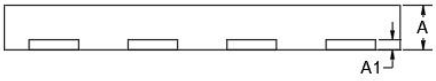
Dimensions(mm)			
Symbol	**Min.**	**Nom.**	**Max.**
A	0.80	0.90	1.00
A1		REF 0.203	
b	0.95	1.00	1.05
D	7.90	8.00	8.10
D1	6.90	7.00	7.10
E	7.90	8.00	8.10
E1	3.10	3.20	3.30
e		REF 2.00	
k		REF 2.80	
L	0.45	0.50	0.55



TOP VIEW



BOTTOM VIEW



SIDE VIEW